

S/N 09/852,839

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

#5 election

Applicant: KONDO et al. Examiner: J. NGUYEN
Serial No.: 09/852,839 Group Art Unit: 2829
Filed: May 10, 2001 Docket No.: 10873.713US01
Title: SEMICONDUCTOR INTEGRATED CIRCUIT, INSPECTION METHOD
FOR THE SAME, AND RECORDING DEVICE AND
COMMUNICATION EQUIPMENT HAVING THE SAME

M. Brunson
2/26/03

CERTIFICATE UNDER 37 CFR 1.10:

"Express Mail" mailing label number: EV 212790712US

Date of Deposit: February 18, 2003

I hereby certify that this paper or fee is being deposited with the United States Postal Service "Express Mail Post Office to Addressee" service under 37 CFR 1.10 on the date indicated above and is addressed to: Assistant Commissioner for Patents, Washington, D.C. 20231.

By: _____

John Brunson

RESPONSE TO RESTRICTION REQUIREMENT

Assistant Commissioner for Patents
Washington, D.C. 20231

Dear Sir:

Applicants elect Group I (claims 1 to 11 and 15, 16, 19 and 20) in response to the
Restriction Requirement of January 30, 2003.

Applicants traverse the requirement to the extent of requesting that claims in
Group II that correspond to allowable Group I claims be reinstated for allowance. An early and
favorable action on the merits is requested.

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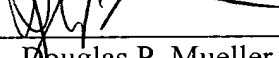
Please charge any additional fees or credit any overpayment to Deposit Account

No. 13-2725.

Respectfully submitted,

MERCHANT & GOULD P.C.
P.O. Box 2903
Minneapolis, MN 55402-0903
(612) 332.5300

Dated: February 18, 2003

By: 
Douglas P. Mueller
Reg. No. 30,300

DPM/gr

